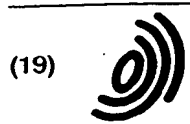


BE



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) EP 0 941 910 A1

(12) **EUROPEAN PATENT APPLICATION**
published in accordance with Art. 158(3) EPC

(43) Date of publication:
15.09.1999 Bulletin 1999/37

(51) Int. Cl.⁶: B62D 5/04, B60R 16/02,
G05B 15/02

(21) Application number: 97943142.6

(86) International application number:
PCT/JP97/03531

(22) Date of filing: 02.10.1997

(87) International publication number:
WO 99/17976 (15.04.1999 Gazette 1999/15)

(84) Designated Contracting States:
DE FR GB

• KISHIMOTO, Yuji-Mitsubishi Denki Kabushiki
Kaisha
Tokyo 100 (JP)

(71) Applicant:
MITSUBISHI DENKI KABUSHIKI KAISHA
Tokyo 100 (JP)

(74) Representative: HOFFMANN - EITLE
Patent- und Rechtsanwälte
Arabellastrasse 4
81925 München (DE)

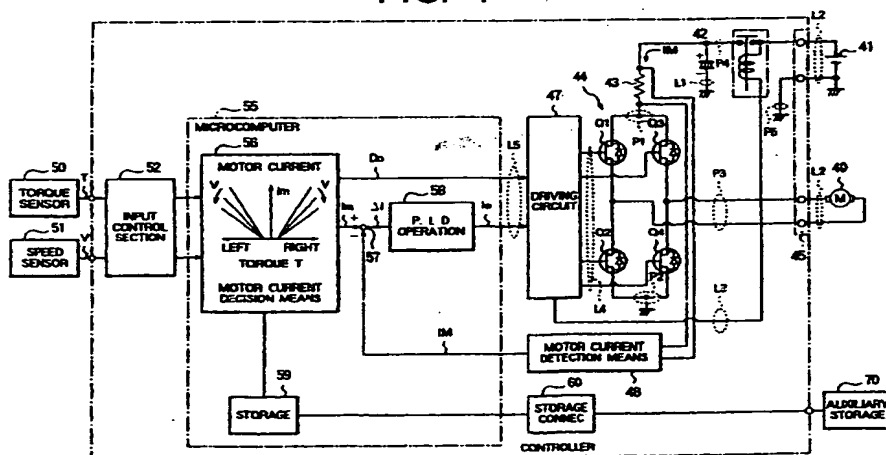
(72) Inventors:
• KOHGE, Shinichi-Mitsubishi Denki Kabushiki
Kaisha
Tokyo 100 (JP)

(54) **CONTROLLER FOR AUTOMOBILE**

(57) A controller for automobile which is provided with a nonvolatile memory which is incorporated in a vehicle control computer and stores control software, and an auxiliary storage device which sets an unerasa-

ble area to the nonvolatile memory and rewrites the control software in an unerasurable area in new control software.

FIG. 1



EP 0 941 910 A1

Descripti n

Technical Field

[0001] The present invention relates to a vehicle controller capable of changing control software to a control algorithm newly developed to improve the performance early and by a relatively inexpensive apparatus in a market after selling a vehicle.

Background Art

[0002] An electric-power-steering controller is described below as one of the conventional vehicle controllers. Figure 3 is a circuit diagram of the conventional power-steering controller disclosed in Japanese Patent Application No. 5-64268 in which a part of the controller is shown by a block diagram. In Figure 3, a motor 40 for outputting an auxiliary torque to a vehicle steering wheel (not illustrated) is driven by a motor current IM supplied from a battery 41. The ripple component of the motor current IM is absorbed by a capacitor 42 having a large capacity (1,000 μ F to 3,600 μ F) and detected by a shunt resistor 43. Moreover, directions and values of the motor current IM are switched in accordance with the operation of a bridge circuit 44 having a plurality of semiconductor switching devices (e.g. FETs) Q1 to Q4 in accordance with the magnitude and direction of the auxiliary torque.

[0003] One end of the capacitor 42 is connected to the ground by a conductive wire L1. The semiconductor switching devices Q1 to Q4 are bridge-connected by wiring patterns P1 and P2 to constitute a bridge circuit 44. Moreover, the wiring patterns P1 and P2 connect the bridge circuit 44 to the shunt resistor 43. The output terminal of the bridge circuit 44 is constituted with a wiring pattern P3.

[0004] The motor 40 and battery 41 are connected to the bridge circuit 44 through the wiring pattern P3 by a connector 45 having a plurality of lead terminals. The motor 40 and battery 41 are connected to the connector 45 by an external wiring L2. The motor current IM is supplied or cut off by a normally-open relay 46 according to necessity. The relay 46, capacitor 42, and shunt resistor 43 are connected each other by a wiring pattern P4. The connector 45 is connected to the ground by a wiring pattern P5. The wiring pattern P3 serving as the output terminal of the bridge circuit 44 is connected to the connector 45.

[0005] The motor 40 is driven by a driving circuit 47 through the bridge circuit 44. Moreover, the driving circuit 47 drives the relay 46. The driving circuit 47 is connected to the exciting coil of the relay 46 by a conductive wire L3. Moreover, the driving circuit 47 is connected to the bridge circuit 44 by a conductive wire L4. The motor current IM is detected by a motor current detection means 48 in accordance with the voltage appearing at the both ends of the shunt resistor 43. The driving circuit

47 and motor current detection means 48 constitute the peripheral circuit element of a microcomputer 55 to be mentioned later.

[0006] The steering torque T of a steering wheel is detected by a torque sensor 50 and the speed V of a vehicle is detected by a speed sensor 51. The microcomputer 55 (ECU) computes an auxiliary torque in accordance with the steering torque T and vehicle speed V, generates a driving signal corresponding to the auxiliary torque by returning the motor current IM, and outputs a rotational direction command D_0 and a current controlled variable I_0 for controlling the bridge circuit 44 to the driving circuit 47 as driving signals.

[0007] The microcomputer 55 is provided with motor current decision means 56 for generating the rotational direction command D_0 for the motor 40 and a motor current command IM corresponding to an auxiliary torque, subtraction means 57 for computing a current deviation ΔI between the motor current command IM and the motor current IM, and PID operation means 58 for computing correction values of P (proportion) term, I (integration) term, and D (differentiation) term in accordance with the current deviation ΔI and generating the current controlled variable I_0 corresponding to a PWM duty ratio.

[0008] Moreover, though not illustrated, the microcomputer 55 includes a publicly-known self-diagnostic function in addition to an AD converter and a PWM timer circuit, always self-diagnoses whether a system normally operates, and cuts off the motor current IM by releasing the relay 46 through the driving circuit 47 when a trouble occurs. The microcomputer 55 is connected to the driving circuit 47 through a conductive wire L5.

[0009] Then, operations of a conventional electric-power-steering controller are described below by referring to Figure 3. The microcomputer 55 captures the steering torque T and vehicle speed V from the torque sensor 50 and speed sensor 51, feedback-inputs the motor current IM from the shunt resistor 43, and generates the rotational direction command D_0 of a power steering and the current controlled variable I_0 corresponding to an auxiliary torque to output them to the driving circuit 47 through the conductive wire L5.

[0010] The driving circuit 47 closes the normally-open relay 46 in accordance with a command through the conductive wire L3 under a normally driving state but it generates a PWM driving signal when the rotational direction command D_0 and current controlled variable I_0 are input and applies the signal to the semiconductor switching devices Q1 to Q4 of the bridge circuit 44 through the conductive wire L4.

[0011] According to the above circuit structure, the motor current IM is supplied from the battery 41 to the motor 40 through the external wiring L2, connector 45, relay 46, wiring pattern P4, shunt resistor 43, wiring pattern P1, bridge circuit 44, wiring pattern P3, connector 45, and external wiring L2. The motor 40 is driven by the

motor current IM to output a required amount of auxiliary torque in a required direction.

[0012] In this case, the motor current IM is detected through the shunt resistor 43 and motor current detection means 48 and returned to the subtraction means 57 in the microcomputer 55 and thereby, controlled so as to coincide with the motor current command Im. Moreover, though the motor current IM includes ripple components because of the switching operation of the bridge circuit 44 under PWM driving, it is smoothed and controlled by the large-capacity capacitor 42.

[0013] A vehicle controller including this type of electric-power-steering controller conventionally uses a microcomputer having a built-in mask ROM storing control software such as control data and control programs.

[0014] However, because it is necessary to secure a predetermined mask ROM fabrication period under short-time system development, it is not temporally permitted to re-fabricate a mask ROM due to re-modification of software specification and it is necessary to early fix the software specification. Therefore, this causes the loads of development engineers to increase.

[0015] Moreover, also when changing control software for a newly-developed control algorithm in order to improve the performance in a market, it is necessary to secure a predetermined mask ROM fabrication period. However, it is impossible to re-fabricate a mask ROM because of changing the control software for the newly-developed control algorithm and to early change the control software in accordance with the newly-developed control algorithm. Furthermore, to reload the control software in a market, it is necessary to prepare an inexpensive auxiliary storage.

[0016] General control software is constituted with the part of discrete corresponding data between an input/output unit connected to a controller and the controller, the part of intrinsic data (e.g. torque-sensor neutral point learning data after final combination of the torque sensor 50 serving as an input/output unit with the controller of a vehicle or trouble history data in the controller mounted on a vehicle in a market after selling the vehicle), and the part of control algorithm.

[0017] Thus, the stored intrinsic data content of the intrinsic data storage block (region) in the control software of a storage to be mentioned later corresponds to each input/output unit and the controller one to one. Therefore, to reload the control software in a market, it is necessary to change the intrinsic data storage block (region) to an unerasable block (region).

[0018] The present invention is made to solve the above problem and its object is to provide a vehicle controller making it possible to relatively inexpensively, easily, early change control software in a market after selling the vehicle correspondingly to performance improvement.

Disclosure of the Invention

[0019]

1. An occasionally-erasable nonvolatile memory built in a vehicle control computer and storing control processing information and auxiliary storage means for setting an unerasable region to the occasionally-erasable nonvolatile memory and updating the control processing information in an erasable region to new control processing information are used.
2. The auxiliary storage means is constituted by adding an information writing function to the occasionally-erasable nonvolatile memory of a troubleshooting unit for reading trouble history information from the memory built in the vehicle control computer.
3. The auxiliary storage means is provided with means for deciding whether the control processing information in the unerasable region of the occasionally-erasable nonvolatile memory is updated.
4. The vehicle control computer is provided with another nonvolatile memory for storing the control processing information in the unerasable region in addition to the occasionally-erasable nonvolatile memory.
5. Storage connection means for setting the occasionally-erasable nonvolatile memory to the erasable mode when the auxiliary storage is connected is used.

Brief Description of the Drawings

[0020]

Figure 1 is a block diagram of the electric-power-steering controller of an embodiment of the present invention;

Figure 2 is a flow chart showing a procedure for updating the control software in the electric-power-steering controller of an embodiment of the present invention; and

Figure 3 is a block diagram of a conventional electric-power-steering controller.

Best Mode for Carrying Out the Invention

[0021] Operations of an electric-power-steering controller of the present invention are described below mainly on the control-software updating operation by referring to the accompanying drawings. Figure 1 is a block diagram of the electric-power-steering controller of this embodiment. Figure 2 is a flow chart showing a procedure for updating the control software of this embodiment. In Figures 1 and 2, a symbol same as that in Figure 3 shows a portion same as or corresponding to that in Figure 3. In Figure 1, symbol 52 denotes an

input control section for inputting a sensor signal to a microcomputer from a torque sensor 50 or speed sensor 51, and 59 denotes a storage built in a microcomputer 55. The storage 59 comprises an occasionally-erasable nonvolatile memory (e.g. flash memory) capable of occasionally updating control software from an external unit. Symbol 60 denotes a storage connection circuit serving as an input/output interface disposed outside of the microcomputer 55 in the controller and 70 denotes an auxiliary storage for accessing the storage 59 through the storage connection circuit 60 to read or update control software. Then, operations of this embodiment are described in accordance with the flow chart in Figure 2. To update control software, the auxiliary storage 70 is first connected to the controller. The storage 59 built in the microcomputer 55 is set to the erasable mode by the storage connection circuit 60 (step S2) when the auxiliary storage 70 is connected (step S1). The auxiliary storage 70 successively designates a block number for updating the control software in the storage 59 (step S3), supplies the update data to the microcomputer 55 in the controller, and the microcomputer 55 updates the control software in the storage 59 (step S4).

[0022] Control software is updated by excluding an intrinsic-data storage block (region) serving as an unerasable block (region). When designated blocks are updated, it is confirmed whether updating of every block to be updated is completed (step S5). Unless updating is completed, step S3 is restated. After updating is completed, the auxiliary storage 70 collates the original data in an unerasable block with the post data after updating other blocks in order to confirm whether the data in the unerasable block is kept in the original state (step S6).

[0023] Moreover, the storage 70 confirms whether the data in the unerasable block is kept in the original state (step S7) and when the data is kept in the original state, it decides that the data in the unerasable block (region) is not updated and completes updating. However, when it is confirmed that the data is not kept in the original state in S7, the storage 70 decides that writing is abnormal and displays a trouble indication on a not-illustrated display portion (step S8).

[0024] However, when storing a part of control software including unerasable data in another nonvolatile memory, it is unnecessary to establish an unerasable block (region) to update control software.

[0025] Then, an inexpensive method for preparing the auxiliary storage 70 is described below which is an object of the present invention. As described above, the controller includes a self-diagnostic function to always self-diagnose whether the system normally operates. If a trouble occurs in the system, the controller stops the system and stores the trouble portion and trouble contents in the memory built in the microcomputer 55.

[0026] Moreover, a not-illustrated troubleshooting unit is prepared at the dealer side of a vehicle so that a trouble portion and its contents can be easily decided when

the system is broken. By connecting the troubleshooting unit to the controller, troubleshooting can be performed. The troubleshooting unit is generally provided with an operating section and a display section and moreover, provided with a function for communication with the microcomputer 55 and a storage.

[0027] The auxiliary storage 70 has many functional sections common to the troubleshooting unit (e.g. operating section; display section; and communicative section). Therefore, by replacing a conventional control software storing mask ROM built in the microcomputer 55 with a flash memory and adding a function which works when control software is changed to update the data in the flash memory to the troubleshooting unit, it is possible to use the troubleshooting unit as the auxiliary storage 70. Moreover, by adding a data-collation software processing function to the troubleshooting unit, an advantage is obtained that error correction of update data can be relatively easily realized.

[0028] Therefore, by remodeling an existing troubleshooting unit and adding two or three functions to the unit, it is possible to inexpensively constitute an auxiliary storage having a data update function compared to the case of newly constituting an auxiliary storage having the data update function.

[0029] As described above, according to this embodiment, an advantage is obtained that the early change of the control software for performance improvement in the electric-power-steering controller can be easily performed by a relatively inexpensive unit also in a market after selling a vehicle by making the control software erasable by the auxiliary storage 70.

[0030] By remodeling an existing troubleshooting unit, it is also possible to inexpensively constitute an auxiliary storage 70 compared to the case of newly constituting an auxiliary storage.

[0031] This embodiment is described about a case of applying the embodiment to an electric-power-steering controller. However, it is needless to say that the same advantage is also obtained by applying the embodiment to another vehicle controller (e.g. vehicle engine controller).

Industrial Applicability

[0032] The present invention provides a vehicle controller making it possible to early change control software by a relatively inexpensive unit also in a market after selling a vehicle by using a control microcomputer having a built-in nonvolatile storage (e.g. flash memory) as a storage for storing the control software.

Claims

1. A vehicle controller comprising an occasionally-erasable nonvolatile memory built in a vehicle control computer and storing control processing information and auxiliary storage means for setting an

unerasable region to said occasionally-erasable nonvolatile memory and updating the control processing information in an erasable region to new control processing information.

- 5
2. The vehicle controller according to claim 1, wherein said auxiliary storage means is constituted by adding an information updating function to an occasionally-erasable nonvolatile memory in a troubleshooting unit for reading trouble history information from a memory built in a vehicle control computer. 10
3. The vehicle controller according to claim 1 or 2, wherein said auxiliary storage means is provided with means for deciding whether the control processing information in the unerasable region of an occasionally-erasable nonvolatile memory is updated. 15
4. The vehicle controller according to claim 1, wherein a vehicle control computer is provided with another nonvolatile memory for storing unerasable control processing information in addition to an occasionally-erasable nonvolatile memory. 20
5. The vehicle controller according to any one of claims 1 to 4, wherein a storage connection means is included which sets an occasionally-erasable nonvolatile memory to the erasable mode when an auxiliary storage is connected. 25 30
- 35
- 40
- 45
- 50
- 55

FIG. 1

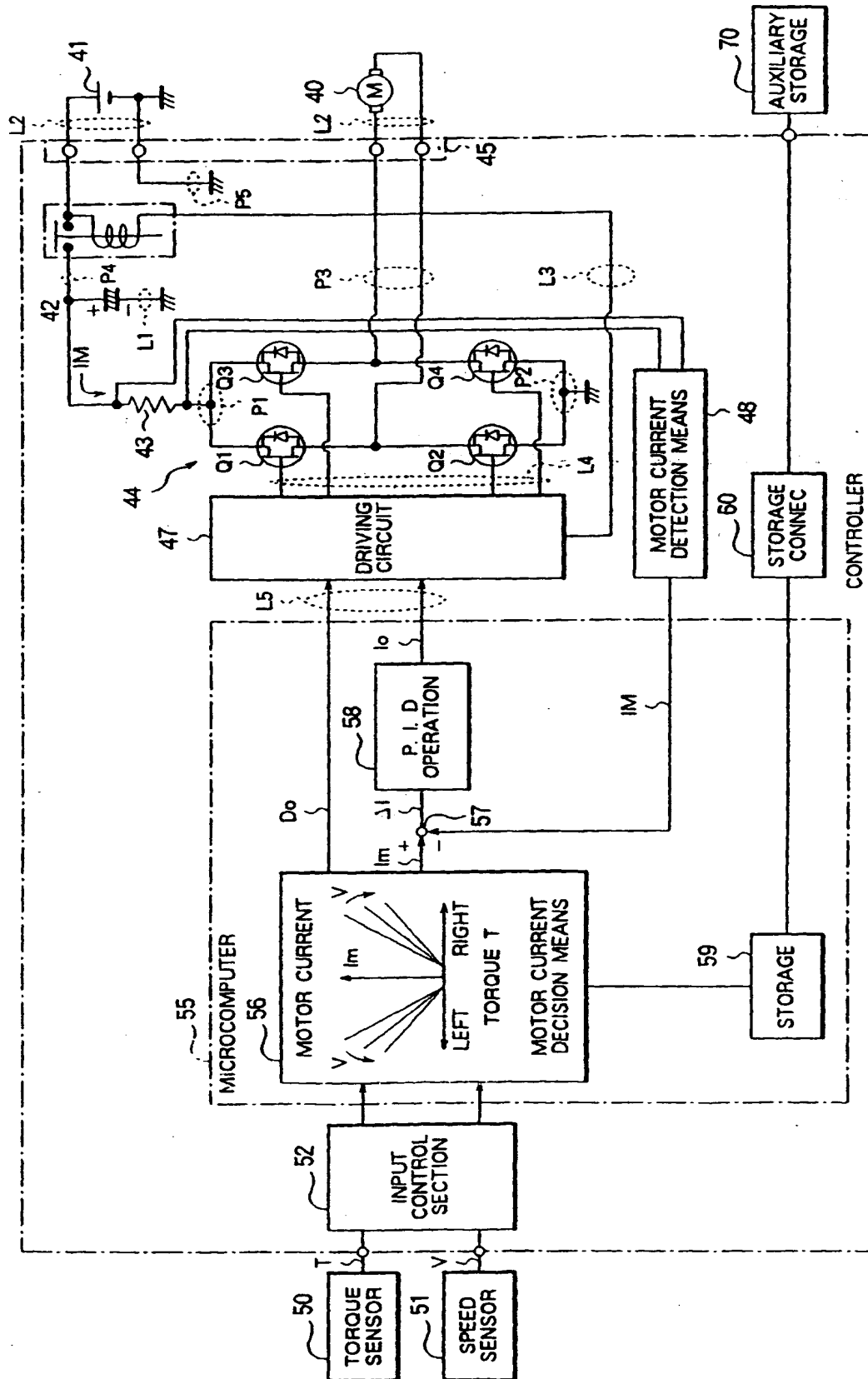


FIG. 2

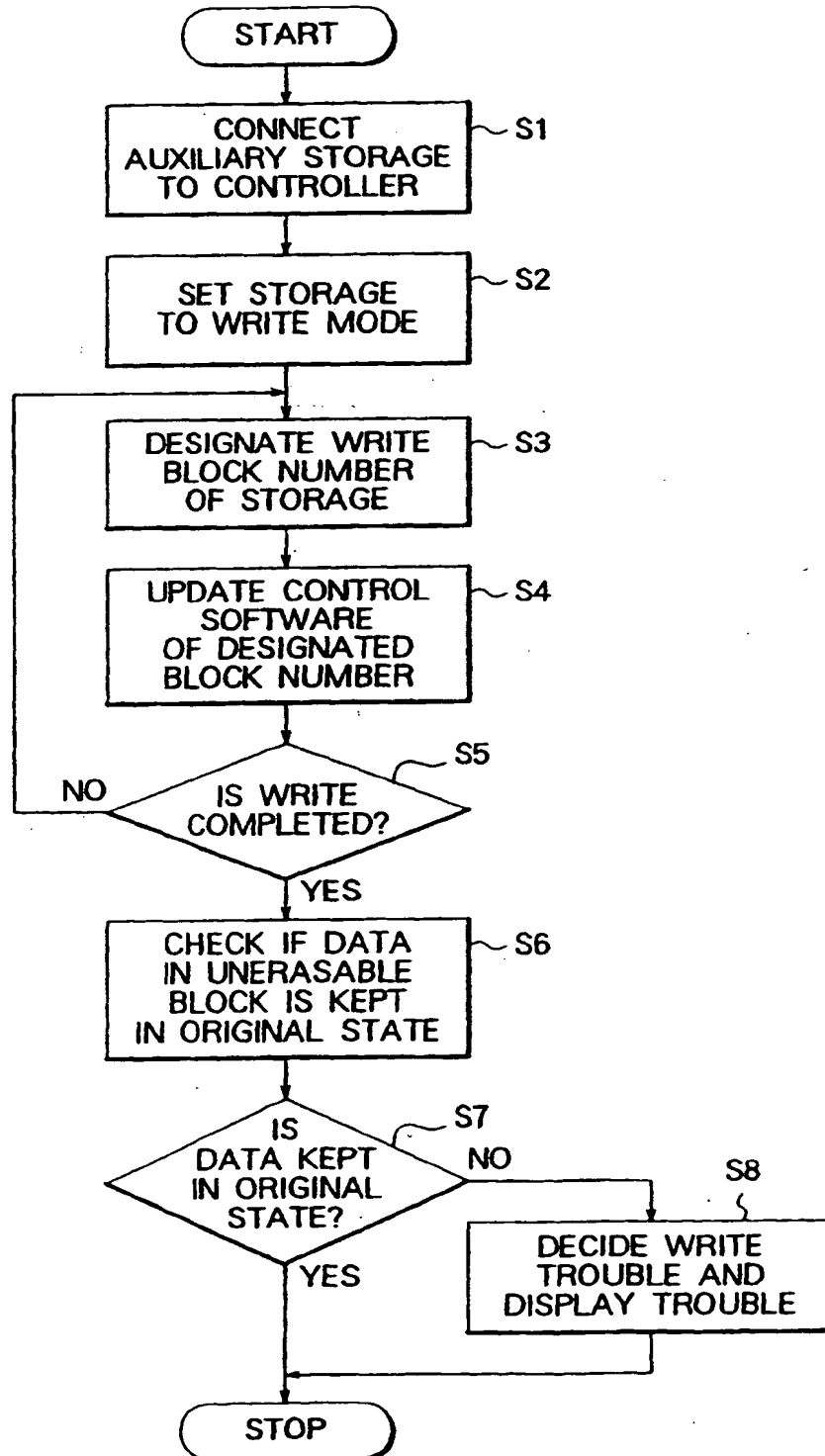
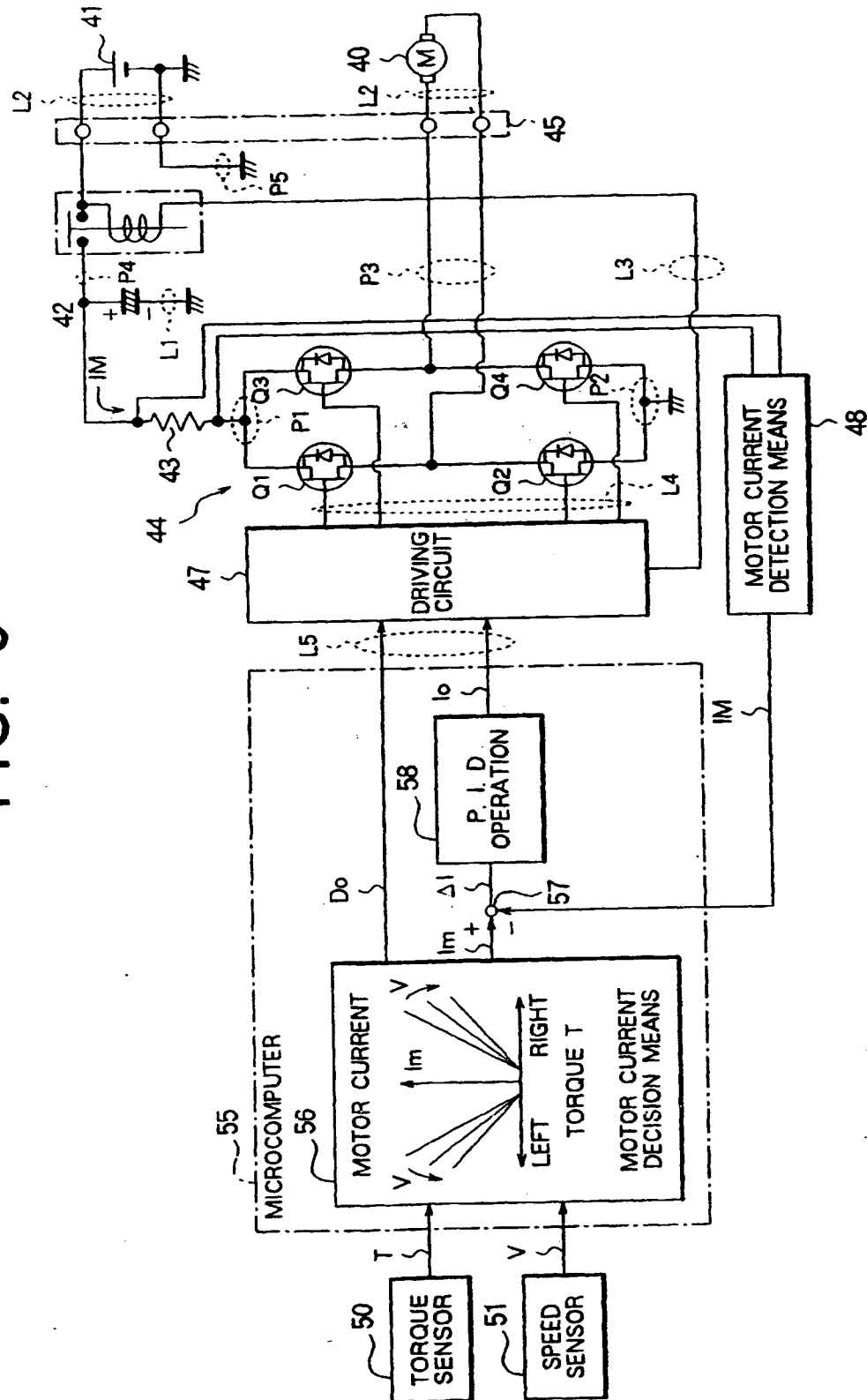


FIG. 3



INTERNATIONAL SEARCH REPORT

International application No.
PCT/JP97/03531

A. CLASSIFICATION OF SUBJECT MATTER Int. Cl ⁶ B62D5/04, B60R16/02, G05B15/02 According to International Patent Classification (IPC) or to both national classification and IPC	
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int. Cl ⁶ B62D5/04, B60R16/02, G05B15/02	
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926 - 1997 Jitsuyo Shinan Toroku Kokai Jitsuyo Shinan Koho 1971 - 1997 Koho 1996 - 1997 Toroku Jitsuyo Shinan Koho 1994 - 1997	
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)	

C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 60-211504, A (Robert Bosch GmbH.), October 23, 1985 (23. 10. 85) & EP, 155403, A & US, 4751633, A & DE, 3410082, A	1 - 5
Y	JP, 04-36048, A (Fuji Heavy Industries, Ltd.), February 6, 1992 (06. 02. 92) (Family: none)	1 - 5
Y	JP, 05-38969, A (Isuzu Motors Ltd.), February 19, 1993 (19. 02. 93) (Family: none)	1 - 5
Y	JP, 63-223901, A (Hitachi, Ltd.), September 19, 1988 (19. 09. 88) (Family: none)	1
Y	JP, 05-147476, A (Robert Bosch GmbH.), June 15, 1993 (15. 06. 93) & DE, 4107052, A	1
Y	JP, 63-90738, A (Fuji Heavy Industries, Ltd.), April 21, 1988 (21. 04. 88) (Family: none)	2

<input type="checkbox"/> Further documents are listed in the continuation of Box C.		<input type="checkbox"/> See patent family annex.	
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reasons (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed		"I" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principles or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document member of the same patent family	
Date of the actual completion of the international search December 9, 1997 (09. 12. 97)		Date of mailing of the international search report December 16, 1997 (16. 12. 97)	
Name and mailing address of the ISA/ Japanese Patent Office Facsimile No.		Authorized officer Telephone No.	

Form PCT/ISA/210 (second sheet) (July 1992)

THIS PAGE BLANK (USPTO)

FIG. 1

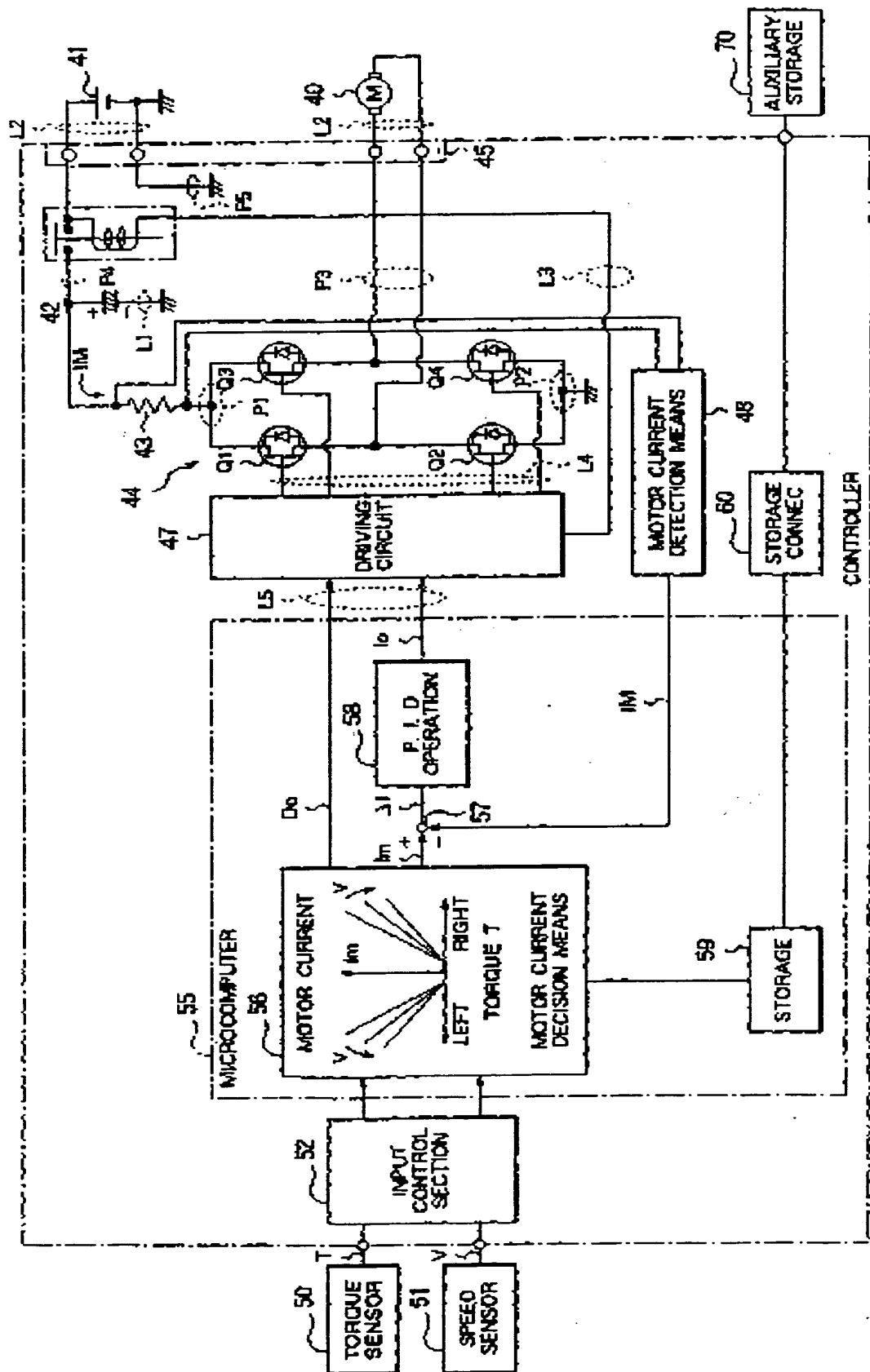


FIG. 2

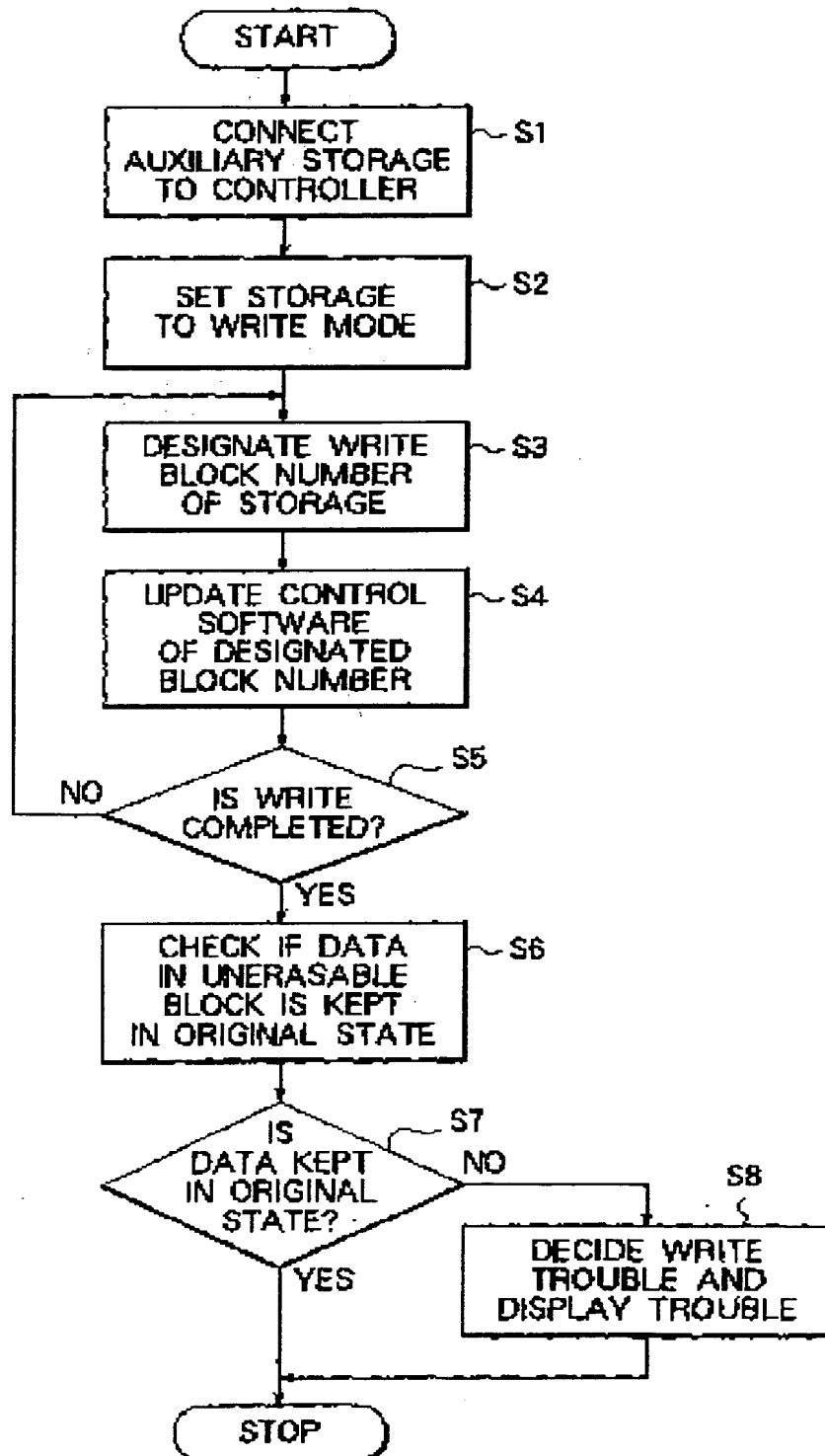
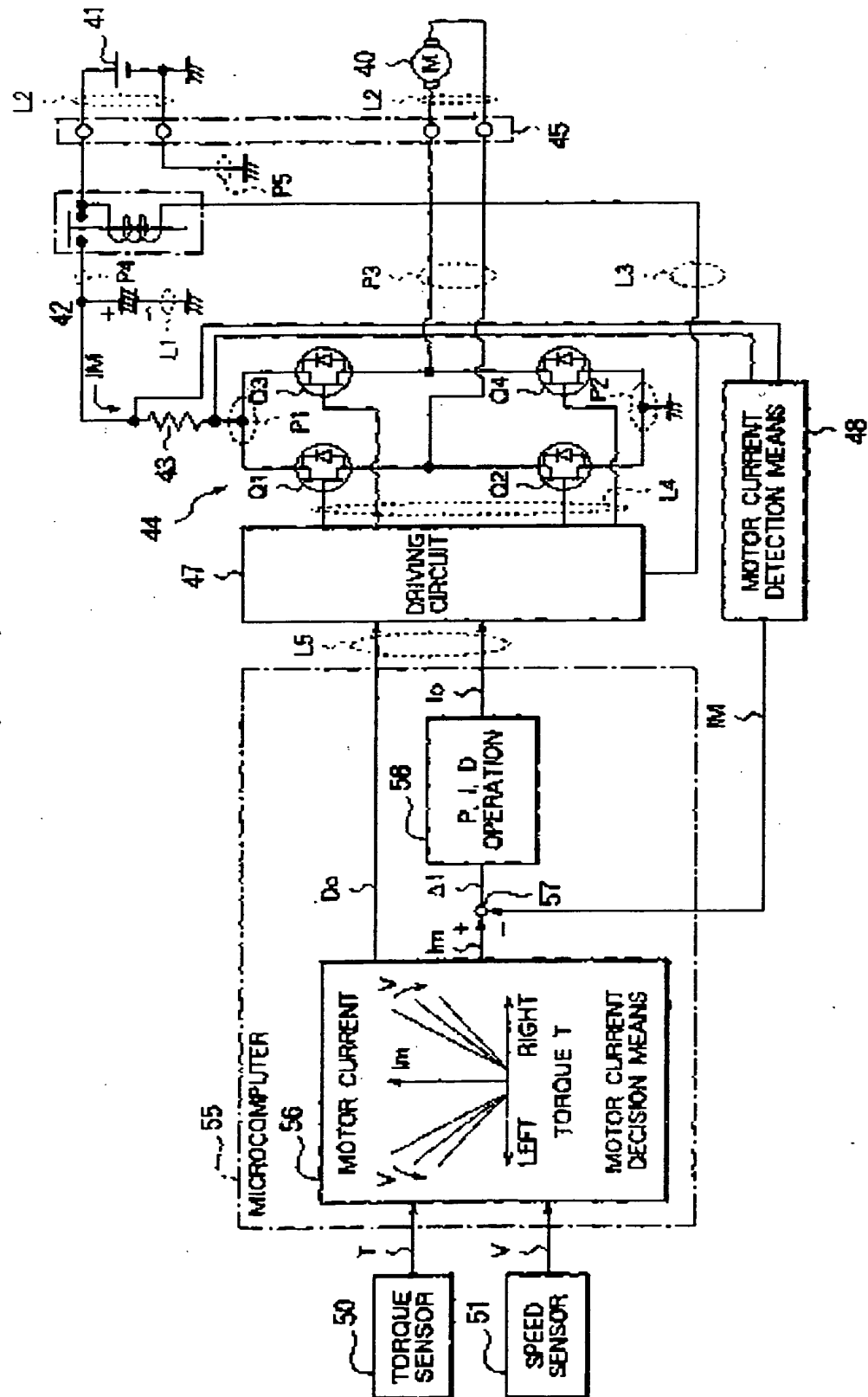


FIG. 3



THIS PAGE BLANK (USPTO)